

B1
Cond. processing circuit,

wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said display panel with said corrected image signal.

B2
Sub C1 2.(Amended) A display device according to claim 1, wherein said display panel is a liquid crystal display panel.

B3
Cond. Sub C1 3.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a source driver circuit, and a gate driver circuit;

an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image signal on a basis of a correction table and feeds said display panel with said image signal on which gamma correction has been performed.

Sub C1
B4 4.(Amended) A method for operating a display device comprising the steps of:
processing an image signal input from an external source by an image signal processing circuit;

feeding pulses directly to said image signal processing circuit and a display panel by a control circuit;

correcting said image signal based on a correction table; and

supplying a corrected image signal to said display panel through a correction circuit.

B5
Sub C1 5.(Amended) A method for operating a display device comprising the steps of:

processing an image signal input from an external source by an image signal processing

35
Panel.
circuit;

feeding pulses directly to said image signal processing circuit and a display panel by a control circuit;

performing a gamma correction of said image signal based on a correction table; and

supplying a corrected image signal to said display panel through a correction circuit.

Sub C1
17.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;

B16
an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit corrects said image signal on a basis of a correction table and feeds said display panel with said corrected image signal.

18.(Amended) A display device according to claim 17, wherein said display panel is a liquid crystal display panel.

Sub C1
22.(Amended) A display device comprising:

a display panel comprising a pixel portion in which a plurality of thin film transistors are arranged in a matrix, a digital video signal dividing circuit, a source driver circuit, and a gate driver circuit;

B17
an image signal processing circuit for processing an image signal input from an external source; and

a control circuit which feeds pulses directly to said display panel and said image signal processing circuit,

wherein said image signal processing circuit performs gamma correction on said image

39
30nd.
signal on a basis of a correction table and feeds said display panel with said image signal on which gamma correction has been performed.

Please add new claims 27-32 as follows:

Sub C1
27.(New) A display device according to claim 1, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

28.(New) A display device according to claim 6, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

30
29.(New) A method according to claim 11, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

30.(new) A method according to claim 14, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

31.(New) A display device according to claim 17, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.

32.(New) A display device according to claim 22, wherein said pulses comprises at least one selected from the group consisting of a start pulse, a clock pulse, and a synchronizing signal.